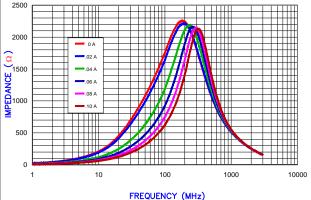
# HZ0603A152R-10

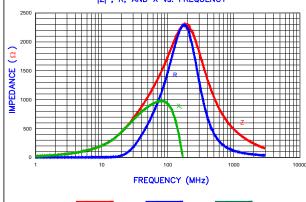
## PHYSICAL DIMENSIONS:

A 1.60 [.063] ± 0.15 [.006] B 0.80 [.031] ± 0.15 [.006] ± 0.15 [.006] C 0.80 [.031] D 0.36 [.014] ± 0.15 [.006]

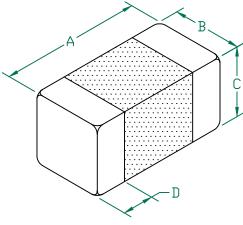
## Z vs. FREQUENCY IMPEDANCE UNDER DC BIAS



|Z| , R, AND X vs. FREQUENCY



AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 5544

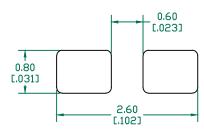


ELECT	STICS:				
Z @ 100MHz ( <u>Ω</u> )		DCR (Ω)	Rated Current		
Nominal	1500				
Minimum	1125				
Maximum	1875	0.90	100 mA		

NOTES: UNLESS OTHERWISE SPECIFIED

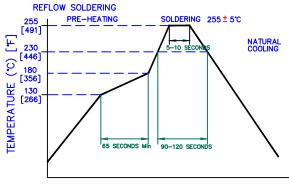
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER CARRIER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

### LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension)

#### RECOMMENDED SOLDERING CONDITIONS



1 of 1

	DIMENSIONS ARE IN mm [INCHES].			This print is the property of Laird Tech. and is loaned in confidence			_	
			subject to return upon request with the understanding that no					H
				copies shall be made without the written consent of Laird Tech. Al		La	ш	A
				rights to design or invention are reserved.				
D	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER:	REV	PART TY	PE: -FIRE	DRAW
С		02/22/08		HZ0603A152R-10		,	-FIRE	
В		07/25/06		1 03/29/06 1	CALE:	_	SHEET:	
Α	ORIGINAL DRAFT	03/29/06	JRK	, ,	OOL #		- 1	of
REV	DESCRIPTION	DATE	INT					

